

# NOAH WILLIAMS

Hardware Development Engineer

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## PROFESSIONAL SUMMARY

Accomplished Hardware Development Engineer with 7 years of experience in FPGA design and hardware optimization. Expertise in VHDL and Verilog, I am dedicated to enhancing system performance and reliability. My strong collaborative skills drive successful project outcomes in cross-functional teams, ensuring innovative solutions that meet technical specifications.

## WORK EXPERIENCE

**Hardware Development Engineer** 📅 Apr / 2020-Ongoing  
**Pineapple Enterprises** 📍 Santa Monica, CA

- 1. Designed and developed hardware components for embedded systems and consumer electronics.
- 2. Integrated features such as automatic gain control and various serial bus protocols, interfacing with ADC, DAC, and digital synthesizers.
- 3. Developed a custom mechanism to trigger FPGA configurations from NOR flash memory using an ARM CPU.
- 4. Collaborated with board design teams for schematic reviews, pinout configurations, and JTAG debugging.
- 5. Ensured compliance with global industry standards through thorough documentation and testing.
- 6. Authored project proposals, requirement specifications, and system architecture documentation.
- 7. Created comprehensive user manuals to facilitate product understanding and usage.

**Hardware Development Engineer** 📅 Apr / 2018-Apr / 2020  
**Cactus Creek Solutions** 📍 Phoenix, AZ

- 1. Engineered digital power controllers for mid-power applications (300kVA+) to enhance efficiency and reliability.
- 2. Conducted board-level hardware design and developed firmware in ASM and ANSI-C, including GUI development in C#.
- 3. Designed and built prototypes for new product revisions and designs, overseeing schematic capture and PCB layout.
- 4. Performed root-cause analysis on non-functional returns, leading to effective board-level revisions.
- 5. Developed hardware prototypes for advanced communication systems, ensuring robust performance.
- 6. Designed complex electronic circuits tailored to project specifications and customer needs.

## EDUCATION

**Bachelor of Science in Electrical Engineering** 📅 Apr / 2016 - Apr / 2018  
**University of California, Berkeley** 📍 Phoenix, AZ

Focused on digital systems, circuit design, and embedded systems development.

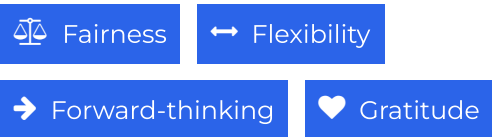
## SKILLS



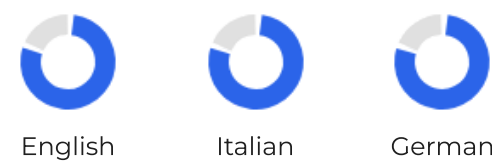
## INTERESTS

- 🎮 Gaming
- 👜 Fashion
- 🎬 Film
- 💻 Technology

## STRENGTHS



## LANGUAGES



## ACHIEVEMENTS

- 🌟 Reduced FPGA design cycle time by 20% through process optimization.
- 🌟 Successfully led a team to develop a multi-protocol communication system, enhancing data transfer efficiency.
- 🌟 Implemented a new testing protocol that improved defect detection rate by 30%.