ROBERT SMITH

PCB layout designer

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SUMMARY

As a PCB layout designer, responsible for Solving electrical and mechanical design issues making sure each PCB design package will correctly interface with all existing mechanical and thermal assemblies Facilitate standardization by being aware of the bigger picture, including design features which will drive standardization and improve efficiency throughout the department.

SKILLS

Technician, Designing Skills, Creative Skills.

WORK EXPERIENCE

PCB layout designer

ABC Corporation - February 2012 – July 2012

- PCB layout designed using Mentor PADS PowerPCB 5.01, Schematic Capt with Orcad and assembly drawings on Autocad.
- Completed high speed PCB layout design and communicating with FAB and Assembly houses to get manufacturing completed.
- Accomplished serial data to parallel data transfer working with SerDes IC.
- Contracted work for a group of small companies.
- Created FAB and assembly drawings.
- Used PADS PCB and ALLEGRO for layout design and Orcad for schematic capture.
- Designed all of VIAs motherboards.

PCB layout designer

Delta Corporation - 2009 – 2012

- PCB layout design.
- Mentor PADS PowerPCB 3.5 and Schematic Capt.
- Layout design for High Speed RF Printed Circuit Boards for wireless phones.
- Communicated with customer interface for discussion of the mechanical applications of the design I was creating.
- Helped customer reduce costs of designs.
- Designed High Speed RF single layer and multi-layer boards using PADS Perform and PADS Power PCB.
- Created schematics with PADS-Power Logic.

SCHOLASTICS

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