



# MASON WILSON

## Senior Design Engineer - Software

✉ support@qwikresume.com

☎ (123) 456 7899

📍 Los Angeles

🌐 www.qwikresume.com

### SKILLS

3d Modeling



Digital Design Engineering



Prototyping And Tooling Design



Supplier Management



Field Testing



Data Analysis Skills



### INTERESTS

📖 Birdwatching 🏠 Traveling

🏋️ Sports Coaching 🧶 Knitting

### STRENGTHS

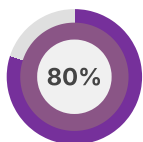
🔧 Pragmatism

🍃 Sensitivity

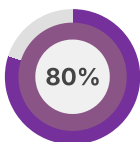
💖 Sincerity

⚓ Stability

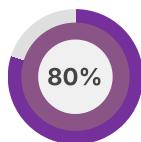
### LANGUAGES



English



Dutch



Arabic

### ACHIEVEMENTS

🌟 Led a team to develop a novel software tool, reducing design cycle times by 30%.

🌟 Streamlined CAD processes, enhancing design accuracy and reducing errors by 25%.

### PROFESSIONAL SUMMARY

Proficient Senior Design Engineer with 7 years of experience in software-driven product development and system architecture. Expert in utilizing advanced CAD tools and innovative design methodologies to deliver high-quality engineering solutions. Proven track record of collaborating across teams to enhance project efficiency and drive continuous improvement initiatives.

### WORK EXPERIENCE

#### Senior Design Engineer - Software

📅 Feb / 2021-Ongoing

WidgetWorks Inc.

📍 Denver, CO

1. Led cross-functional teams to develop innovative design solutions for complex engineering projects.
2. Created various digital circuits to meet the specific requirements of internal stakeholders.
3. Conducted circuit design at the transistor level, including timing verification and characterization.
4. Generated and validated IBIS models for input/output buffer specifications.
5. Measured IV data for circuit components, ensuring accurate correlation with simulation models.
6. Developed VHDL models for custom circuits, successfully simulating and verifying functionality.
7. Collaborated with cross-functional teams to enhance product design and development processes.

#### Senior Design Engineer

📅 Feb / 2018-Feb / 2021

Crescent Moon Design

📍 Portland, OR

1. Collaborated with marketing and engineering teams to define specifications for PCI Bus expansion boards.
2. Designed and tested prototype boards using VHDL and various FPGA technologies.
3. Served as Lead Engineer for the Pro Tools PCI DSP Farm board, overseeing all design phases.
4. Managed schematic capture, PCB layout, and prototype integration for new products.
5. Resolved interfacing issues and ensured signal integrity across complex systems.
6. Implemented robust clock generation and control logic, optimizing digital audio interfaces.

### EDUCATION

#### Master of Science in Electrical Engineering

📅 Feb / 2015-Feb / 2018

University of California, Berkeley

📍 Seattle, WA

Focused on advanced circuit design and digital systems.