



CHARLOTTE HARRIS

Asst. Senior Hardware Engineer

support@qwikresume.com

(123) 456 7899

Los Angeles

www.qwikresume.com

PROFESSIONAL SUMMARY

An exceptionally well-rounded engineer with proven problem-solving abilities and a results-oriented attitude. Experienced in power management and system architecture, project leadership, team leadership and training for some of the premier companies in their industry. Demonstrated capabilities include architectural definition, extensive debugging, pre- and post-silicon validation, training of team members.

WORK EXPERIENCE

Asst. Senior Hardware Engineer

May / 2019-Ongoing

Blue Sky Innovations

Chicago, IL

1. Provided Video Capture solutions for Intel's next-generation microprocessor by resolving video capture issues that had existed for 2 years.
2. Presented findings and white paper at Intel Developers Forum.
3. Responsible for developing Comdex demo for the Instantly Available Architecture including debugging external vendor's silicon and drivers.
4. Provided demo and support for Andy Grove's keynote speech at Agenda '98.
5. Responsible for an architectural overview of the Digital Visual Interface (DVI), a standard developed by Intel, IBM, Compaq, HP, Fujitsu, NEC, and Silicon Image.
6. Responsible for enabling the memory interface on the first 100 MHz motherboard including working with external silicon vendors to debug their silicon and ensure PC100 specification compliance, correcting errors in the SPD and BIOS specification, debugging BIOS initialization code for 100 MHz SPD enumeration.
7. Defined Folsom Design Center's Desktop Platform validation methodology.

Senior Hardware Engineer

May / 2015-May / 2019

Lakeside Apparel Co

Chicago, IL

1. Low Power Design > Power analysis and methodology development > Power modeling for system cache, memory subsystem and interconnect fabrics > IP power.
2. Designed and developed USB related products (silicon IC) from concept to mass production, and customer support.
3. Selected Accomplishments: ISP1161 Single Chip USB Host & Device Controller development from concept design to volume production.

EDUCATION

Master of Science in Electrical Engineering

May / 2012 - May / 2015

University of California, Berkeley

Chicago, IL

Focused on advanced hardware design and embedded systems.

SKILLS

MS Office



Autocad



Formerly Licensed Realtor



Digital Circuit Design



Analog Circuit Design



INTERESTS

Language Learning Photography

Technology Gardening

STRENGTHS

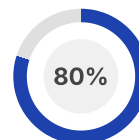
Competitiveness

Gratitude

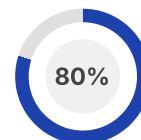
Dedication

Determination

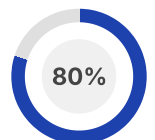
LANGUAGES



English



Russian



Swahili

ACHIEVEMENTS

Led the successful design of a high-speed PCB for next-generation processors, reducing production time by 20%.

Developed a robust testing protocol that improved product reliability by 30%, ensuring compliance with industry standards.

Spearheaded a cross-functional initiative that enhanced collaboration between engineering and production teams, resulting in a 15% decrease in time-to-market.