Robert Smith

Senior Principal Engineer II

CONTACT DETAILS

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PERSONAL STATEMENT

killed engineer with deep knowledge of hardware, systems and software development processes Aptitude for solving complex problems in a regulated environment Experienced in every phase of software and product development lifecycle Strong leader of cross-functional teams that develop cutting edge products, technologies and processes.

WORK EXPERIENCE

Senior Principal Engineer II

Avago Technologies - May 2015 – 2020

Responsibilities:

- Facilitates, Connects, and Unify Program/Product Management to independent, worldwide engineering organizations utilizing HP ALM (Application LifeCycle Management, HP Quality Center).
- Manages Software Quality Assurance and Design Verification activities for Oracle Fibre Channel OEM acceptance.
- Investigates, experiments, and develop new or advanced concepts, applications, and techniques.
- Applies highly innovative techniques to technical problems or product definition and design.
- Develops and applies state of the art engineering, programming, and scientific theories and methods to the solution of complex technical problems.
- Conducts investigations and design test approaches to the development of new designs, methods, or processes requiring the application of advanced physical, chemical, electrical or mathematical theory.
- Analyzes methods and establish theoretical principles and empirical test procedures to develop solutions.

Senior Principal Engineer

Delta Corporation - 2012 – 2015

Responsibilities:

- Team Leader and Software Developer for a Digital Microscope Camera project Implemented software that captures, enhances, saves, and prints images .
- Procurement of technology - LCDs, digitizers, battery packs, components, chips Computer (PCs, notebooks, pen-based) design & development, with .
- Spearheaded the systems integration effort for a "pen-based" computer system design.
- Finished product development in a partnership with Samsung Electronics, Korea.
- Responsible for the selection of critical modules and negotiated terms for their acquisition.
- Present responsibilities include leading design-stage and new Process Hazard Analyses (PHAs) and PHA revalidations or re-dos, conducting PSM and RMP .

SKILLS

Engineering
Management,
Organizing.

LANGUAGES

English (Native)
French (Professional)
Spanish (Professional)

INTERESTS

Climbing
Snowboarding
Cooking
Reading

REFERENCES

Reference – 1 (Company Name)
Reference – 2 (Company Name)

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- Responsibility - Design, development and integration of two embedded High Speed multi-core microprocessor modules including PowerQUICC and ARM CPUs, .

Education

Electronics Engineering Technology - (ITT Techincal Institute)

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