ROBERT SMITH

Sr.Test Development Engineer

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SUMMARY

Dedicated and results oriented professional with 33 years experience in the automotive industry. Demonstrated management experience in several functional areas including engineering, validation, test development, facilities / laboratory management and systems program management. Leadership recognized ability to maintain focus on development and execution of strategies to achieve organizational objectives on time and within budget.

CORE COMPETENCIES

C, C++, Python, Java, Semiconductors, Agile, Testing, Failure Analysis.

PROFESSIONAL EXPERIENCE

Sr.Test Development Engineer

ABC Corporation - 2000 – 2015

Key Deliverables:

- Developed software used to investigate compatibility issues, an R&D effort used for contract awarding purposes.
- Performed full vehicle and sub-system durability simulation using RPC (Remote Parameter Control).
- Developed test methods which include innovative test fixture design to simulate the proving ground environment in the lab using physical test .
- Developed test solutions on Teradyne Micro Flex and Unix based ATEs.
- Responsible for verifying all codes on third party NVM IP.
- Designed and implemented interface hardware for VXI test platform.
- Wrote program in C generating and reading patterns to qualify integrity of NVM cells.

Test Development Engineer

Delta Corporation - 1998 – 1999

Key Deliverables:

- Lead test engineer for design of Automatic Test Equipment for satellite flight hardware.
- Developed three bay automated test console for testing complete satellite flight assembly, Tasks included vetting and purchasing VXI instruments from third party vendors and writing drivers for each instrument.
- Complete program was written in C in a Wind Rivers Tornado VxWorks environment.
- Program covered all components of in-flight hardware including up/downlink telemetry and operation of data/power management systems.
- Tasks included design of interface hardware and signal conditioning components.
- Final test release resulted in successful launch of orbital satellite space system.
- Developed test programs in C and assembly language on embedded test systems to catch defects in Nand memory products Constantly enhancing program .

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EDUCATION

- BS in Electrical Engineering - (San Francisco State University - San Francisco, CA)

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